

Substitute for form 1449A/PTO <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> (use as many sheets as necessary)		<b>Complete if Known</b>	
Application Number		10/774,151	
Filing Date		February 5, 2004	
First Named Inventor		Christian Lutkemeyer	
Group Art Unit		2819	
Examiner Name		Not assigned <i>Longland</i>	
Attorney Docket Number		15308US01	
Sheet	1	of	1

U.S. PATENT DOCUMENTS					
Examiner Initial*	Cite No. <sup>1</sup>	Document Number Number-Kind Code <sup>2</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
	A1				
	A2				
	A3				
	A4				
	A5				

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Country Code <sup>3</sup>	Number <sup>4</sup> -Kind Code <sup>5</sup> (if known)			
	A6					
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<i>JB JL</i>	A11	MULTIPLIER, "The multiplier," pp. 9-1 to 9-18
<i>JB JL</i>	A12	COMPUTER SYSTEMS LABORATORY, STANFORD UNIVERSITY, "Lecture 9: Multipliers," pp. 9-1 to 9-27
<i>JB JL</i>	A13	<a href="http://www.electrotec.com/Gigabit_Explained.htm">http://www.electrotec.com/Gigabit_Explained.htm</a> , "1000BASE-T OVERVIEW," November, 2003, pp. 1-5
<i>JB JL</i>	A14	<a href="http://www.elecdesign.com/Globals/PlanetEE/Content/2709.html">http://www.elecdesign.com/Globals/PlanetEE/Content/2709.html</a> , "Gigabit Ethernet PHY Chip Sets LAN Speed Record For CopperStory," November 1998, pp. 1-6
	A15	

EXAMINER SIGNATURE	<i>Dean Bunker Longland</i>	DATE CONSIDERED	10/22/04
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